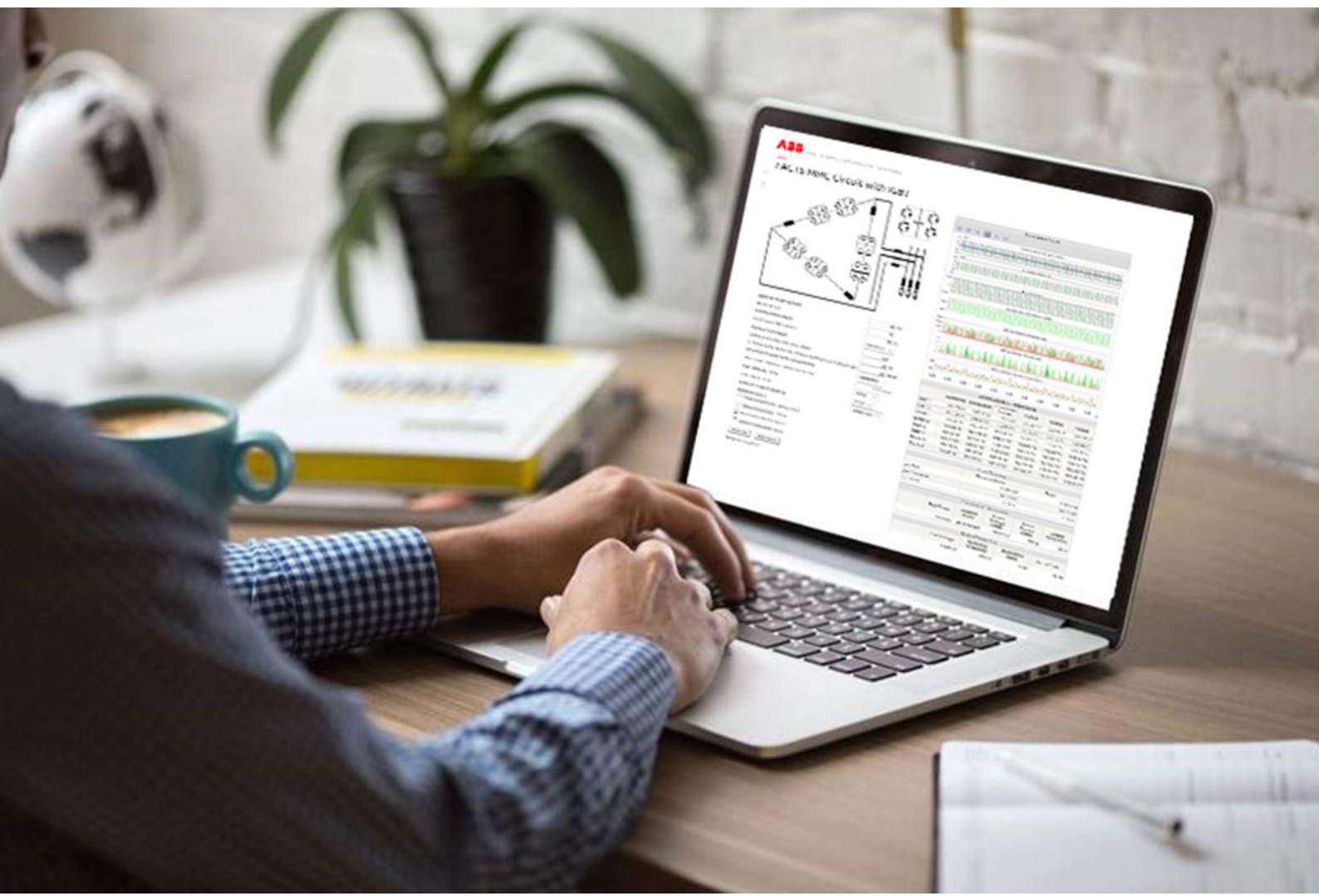

SEMIS Simulation Tool FACTS Full Bridge MMC with IGBT User manual



INTRODUCTION

SEMIS is a web-based semiconductor simulation tool providing a thermal calculation of the semiconductor losses for common converter circuits. The simulation simplifies significantly the selection of the switching device and enables the optimal selection of semiconductors for further investigations.

The SEMIS Simulation Tool is a user-friendly online application found on ABB Semiconductors website www.abb.com/semiconductors/semis

SEMIS users select from a substantial selection of topologies. By assigning the circuit parameters and selecting the desired switching device, multiple ABB products can be simulated at the same time. Once a simulation is run, SEMIS returns comprehensive results on semiconductor losses as well as on the electrical parameters in the input and output of the circuit. The results are shown in both graphical (waveforms) and numerical (tables) way.

The SEMIS tool is based on the PLECS simulation software. PLECS users can download our product models in the XML file format from the ABB Semiconductors website and use them for their own simulations. For more specific topologies ABB offers customized converter simulations for non-standard topologies with PLECS simulation software on a project basis.

COPYRIGHTS

All rights to copyrights, registered trademarks, and trademarks reside with their respective owners.

Copyright © 2020 ABB Power Grids Switzerland Ltd.

All rights reserved.

Release: April 2020

Document number: 5SYA 2127

TABLE OF CONTENTS

1. FACTS FULL BRIDGE MMC WITH IGBT	6
2. OVERVIEW	7
2.1 FACTS Full Bridge MMC with IGBT power circuit schematic	8
3. SIMULATION SETTINGS	9
3.1 Circuit parameters	9
3.1.1 Ambient temperature	9
3.1.2 Controller	9
3.1.3 Load parameters	9
3.2 Switch settings	10
3.2.1 Matching IGBTs	10
3.3 Selection of Articles / Start simulation.....	11
4. SIMULATION RESULTS.....	12
4.1 Graphical Output – Waveforms	12
4.1.2 Control.....	13
4.1.3 Parameters values indication	13
4.2 Numerical / Tabular results	13
5. ALERTS & FEATURES.....	16
5.1 Junction Temperature	16
5.2 Cell Voltage	16
6. APPLIED CALCULATIONS	17
6.1 Input Parameter Definitions	17
6.2 Line-Line RMS Voltage of Grid/Load Definition.....	17
6.3 Real Power	17
6.4 Reactive Power	18
7. VALIDATION OF SEMIS RESULTS WITH ACTUAL FIELD DATA	19
8. USER MANUAL REVISION HISTORY	21
9. SIMULATION SOFTWARE RELEASE HISTORY.....	21

LIST OF FIGURES

Figure 1 FACTS Full Bridge MMC with IGBT circuit on website	7
Figure 2 FACTS Full Bridge MMC Star & Delta Connected with IGBT schematic layout	8
Figure 3 Ambient temperature input block.....	9
Figure 4 Controller input block.....	9
Figure 5 Grid/Load parameter input blocks	9
Figure 6 Thermal settings and IGBT selection	10
Figure 7 Matching IGBTs for selection	10
Figure 8 Start of simulation	11
Figure 9 Simulation progress and termination	11
Figure 10 Graphical results of FACTS Full Bridge MMC with IGBT	12
Figure 11 Tabular indication of cursor position graph values	13
Figure 12 Device Losses & Temperatures	14
Figure 13 Definition of T_{vj} before the last switch	15
Figure 14 Converter AC Parameters	15
Figure 15 Control Parameters	15
Figure 16 Validation of SEMIS results with Field Data for FACTS Full Bridge MMC with IGBT Star Connected.....	19
Figure 17 Validation of FACTS Full Bridge MMC Delta Connected with Star Connected	20

1. FACTS FULL BRIDGE MMC WITH IGBT

The use of powerful modular multilevel converters (MMC) for FACTS applications has resulted in the development of new STATCOM products, due to its superior characteristics and competitive costs. The converter module comprises of 3 phases with each phase having a several IGBT based full-bridge cells are connected in series to build up the requisite voltage. Each arm contains the phase reactor. The MMC simplifies equipment design, improves response time and reduces losses.

ABB offers the following Three-phase topologies for thermal analysis simulation with

- Three-Phase Two-level VSC with IGBT
- Three-Phase Three-level VSC with IGBT (NPC, TNPC, ANPC)
- Three-Phase Three-level VSC with IGCT (NPC, TNPC, ANPC)
- Three-Phase Three-level VSC with IGBT Half-Bridge MMC
- Three-Phase Three-level VSC with IGCT Half-Bridge MMC
- Three-Phase Three-level VSC with IGCT Full Bridge MMC
- Three-Phase Three-level VSC FACTS with IGBT Full Bridge Star Connected
- Three-Phase Three-level VSC FACTS with IGCT Full Bridge Star Connected
- Three-Phase Three-level VSC FACTS with IGBT Full Bridge Delta Connected
- Three-Phase Three-level VSC FACTS with IGCT Full Bridge Delta Connected

2. OVERVIEW



Figure 1 FACTS Full Bridge MMC with IGBT circuit on website

Grid definitions

Converter settings

IGBT selection

Results graphs

Results tables

2.1 FACTS Full Bridge MMC with IGBT power circuit schematic

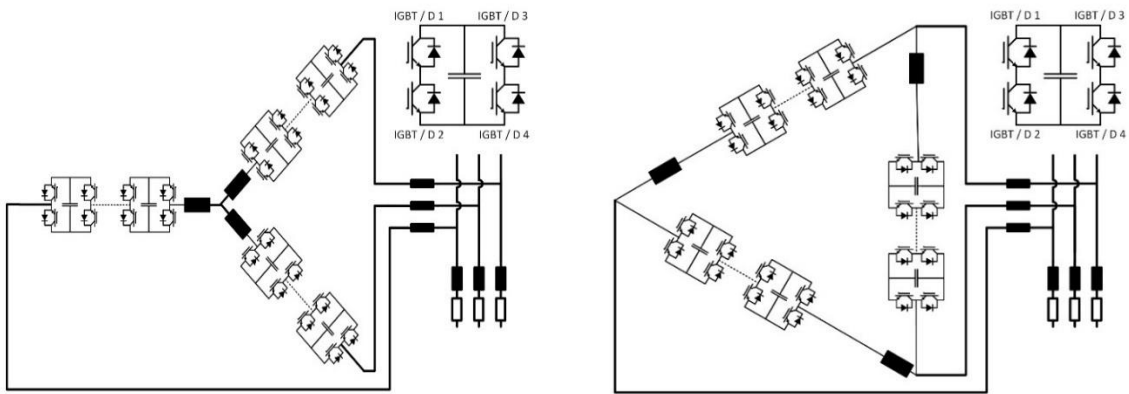


Figure 2 FACTS Full Bridge MMC Star & Delta Connected with IGBT schematic layout

3. SIMULATION SETTINGS

3.1 Circuit parameters

3.1.1 Ambient temperature

Ambient temperature	Definition of the environmental temperature around the converter for temperature / cooling calculations	Range -25 .. 90 °C
---------------------	---	--------------------

AMBIENT TEMPERATURE: °C

Figure 3 Ambient temperature input block

3.1.2 Controller

The user can define the following parameters as seen in figure 4. The controller generates the switching pulses for the upper and lower IGBTs of the cell in the converter.

NO.OF CELLS:

SYSTEM FREQUENCY: Hz

SWITCHING FREQUENCY: Hz

MODULATION INDEX:

Figure 4 Controller input block

NO. OF CELLS	No. of cells in a phase leg	Range 2 to 20
SYSTEM FREQUENCY	Converter AC output frequency	Range 10 to 100 Hz
SWITCHING FREQUENCY	Definition of switching frequency applied for PWM control (Phase-shifted PWM)	Range 86 to 500 Hz
MODULATION INDEX	Definition of modulation index	Range 0 .. 1

3.1.3 Load parameters

The user can define the following parameters as seen in figure 5.

Converter Line Voltage (RMS): kV

Converter Reactive Power: MVAR

Reactive Power Type (Converter):

Figure 5 Grid/Load parameter input blocks

CONVERTER LINE VOLTAGE (RMS)	AC Line voltage of converter	Range 1 .. 1000 kV
CONVERTER REACTIVE POWER	Reactive Power supplied by converter	Range 1 .. 1000 MVAR

Simulation Settings

REACTIVE POWER TYPE

Selection

Reactive power type can be selected as Inductive or Capacitive based on load requirement

3.2 Switch settings

HEAT SINK THERMAL RESISTANCE IGBT:	<input type="text" value="0.02"/> K/W
IGBT MODULE TYPE:	HiPak <input type="button" value="v"/>
IGBT SELECTION:	4.5 kV <input type="button" value="v"/>
MODULE CONFIGURATION:	Single IGBT <input type="button" value="v"/>

Figure 6 Thermal settings and IGBT selection

Heat Sink Thermal Resistance

Range 0.0001 .. 0.5 K/W

Definition of thermal resistance of the cooling system applied.

Remark:

Include the thermal resistance of case to heatsink to ensure correct simulation results. The value entered is attributed to each individual switch shown in the electrical configuration schematic of the IGBT module datasheet. Therefore, if a user selects a dual switch module, the Rth should be multiplied with a factor of 2 to differentiate from the single switch case, if the same heatsink would be used in both cases. Same applies for the case of full bridge modules.

The selected Rth is also accounted for the antiparallel diode position for which same consideration applies for its electrical configuration.

IGBT module type

Select housing type of IGBT for filtering

Selection

IGBT selection

Select voltage class of IGBT for filtering

Selection

Module configuration

Select topology of IGBT module for filtering

Selection

3.2.1 Matching IGBTs

Once the previous IGBT properties are selected, the matching IGBT options appear. By clicking on the product code name the user may access the datasheet from the ABB website.

Matching IGBTs:

- [5SNA 0650J450300](#) 650 A
- [5SNA 0800J450300](#) 800 A
- [5SNA 1200G450300](#) 1200 A
- [5SNA 1200G450350](#) 1200 A

Figure 7 Matching IGBTs for selection

Up to 4 elements can be selected simultaneously and simulated. If one or more elements produce results exceeding the safe operating area (SOA), no results are returned. In this case, the user should run the simulation again with changed parameters and/or product selection to enable results within SOA operating conditions.

3.3 Selection of Articles / Start simulation

To simulate one or more articles, select from the list by activating the checkbox

Simulate	Starts the simulation The progress of the simulation is shown with a number of calculated Jacobian.
Abort	Stops the simulation; No results generated
Hold results	To compare multiple simulations, results can be held for later viewing By selecting the button, result are hold after the simulation has finalized for later comparison with succeeding simulations



Figure 8 Start of simulation



Calculate Jacobian: 7/15

Figure 9 Simulation progress and termination

Simulation Results

4. SIMULATION RESULTS

The simulation results are displayed in two different ways for all selected articles simulated.

Graphical results	Visual analysis of waveforms for fast and efficient detection of most significant sources
Numerical results	Numeric indication of all simulations values for direct comparison

Remark: To hide curves of selected articles, unselect in the table “Results History”

4.1 Graphical Output – Waveforms

When the simulation finishes the semiconductor and AC side waveforms are shown as follows:

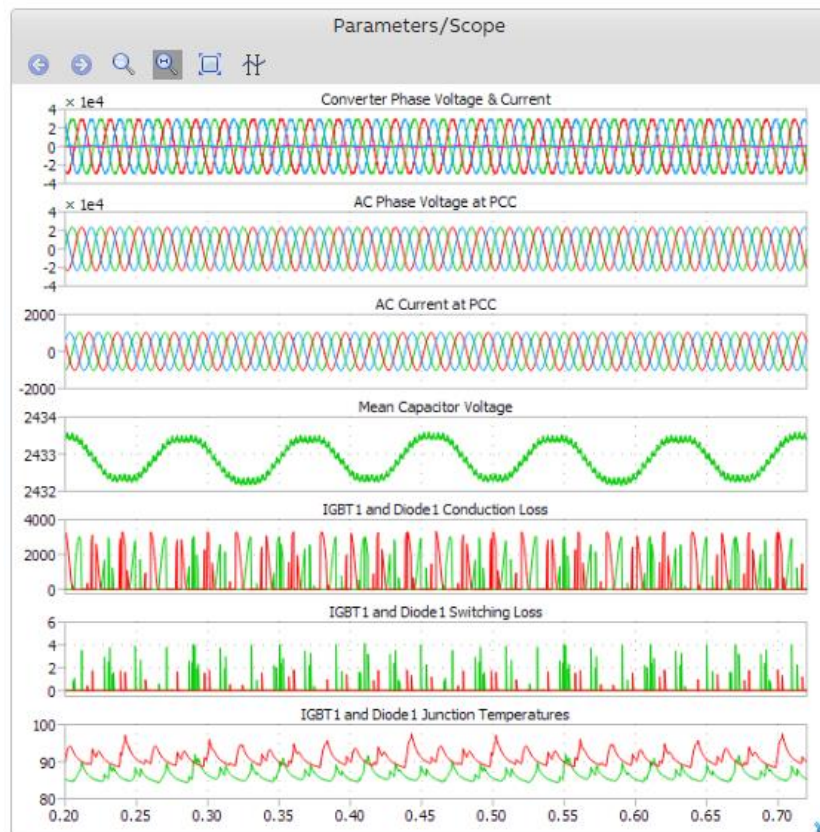







Figure 10 Graphical results of FACTS Full Bridge MMC with IGBT

4.1.2 Control

For an indication of values within the graph, a cursor can be activated to show curve values in a table. Sections of graphs can be zoomed in by click, move and release mouse button for more details

	Hide selectively waveforms of products
	Rest zoom to full view
	Activate cursors and to show parameter values table according to the cursor position
	Zoom selectable rectangle
	Zoom horizontal or vertical band

4.1.3 Parameters values indication

Tabular indication of graphical waveforms values according to cursor position selected. Values are indicated for each parameter. The third column shows the difference between the two cursors per parameter.






















Name		Cursor 1	Cursor 2	Delta	
Time		0.37333	0.54667	0.17333	
Converter Phase Voltage & Current					
Measured voltage:1		-2.685e+4	2.442e+4	-5.126e+4	
Measured voltage:2		2.434e+4	0.000	2.434e+4	
Measured voltage:3		0.000	-2.686e+4	2.686e+4	
Measured current:1		513.0	509.8	3.175	
Measured current:2		509.7	-1023	1532	
Measured current:3		-1023	512.8	-1535	
AC Phase Voltage at PCC					
Measured voltage:1		-2.036e+4	2.036e+4	-4.073e+4	
Measured voltage:2		2.036e+4	0.00008379	2.036e+4	
Measured voltage:3		0.00002118	-2.036e+4	2.036e+4	
AC Current at PCC					
Measured current:1		513.0	509.8	3.175	
Measured current:2		509.7	-1023	1532	
Measured current:3		-1023	512.8	-1535	
Mean Capacitor Voltage					
Gain2		2433	2433	-0.01447	
IGBT1 and Diode1 Conduction Loss					
IGBT conduction loss		0.000	1065	-1065	
Diode conduction loss		0.000	0.000	0.000	
IGBT1 and Diode1 Switching Loss					
IGBT switching loss		0.000	0.000	0.000	
Diode switching loss		0.000	0.000	0.000	
IGBT1 and Diode1 Junction Temperatures					
IGBT junction temp		87.89	84.60	3.287	
Diode junction temp		89.10	91.11	-2.020	

Figure 11 Tabular indication of cursor position graph values

Remark:

The numerical values of each indicated parameter are shown according to the position of the respective cursor in the graph. Drag cursor to investigate about full details.

4.2 Numerical / Tabular results

The following parameters are given in a tabular format in multiple sections.

The indicated elements in the table IGBT1 etc. correspond to the different semiconductor positions in a full-bridge cell as shown in Figure 2.

As converter losses, the aggregated losses in all 3 phase legs are accounted for.

Simulation Results

In addition to the semiconductor losses, there are also losses occurring in the passive components (e.g. Resistances, grid-impedances, etc.). These Losses are not taken into consideration for this simulation. For the simplicity of the simulation, it is assumed that all semiconductors in one phase leg are loaded symmetrically and no voltage asymmetries do exist.

Device losses and temperatures

Device Losses & Temperatures						
	Switching	Conduction	Combined Losses	TvjAvg	TvjMax	TvjBLS
IGBT 1	484.17 W	417.08 W	901.25 W	86.29 °C	92.01 °C	90.01 °C
IGBT 2	483.66 W	417.28 W	900.94 W	86.30 °C	90.39 °C	88.92 °C
Diode 1	102.72 W	475.15 W	577.87 W	91.00 °C	97.51 °C	96.24 °C
Diode 2	103.56 W	475.10 W	578.66 W	91.04 °C	96.82 °C	95.30 °C
IGBT 3	497.58 W	420.02 W	917.60 W	86.84 °C	93.50 °C	91.44 °C
IGBT 4	471.68 W	417.91 W	889.59 W	85.86 °C	91.39 °C	89.45 °C
Diode 3	100.11 W	473.63 W	573.74 W	91.09 °C	96.93 °C	95.60 °C
Diode 4	106.09 W	472.79 W	578.88 W	90.82 °C	97.15 °C	95.72 °C

Loss Summary		
	Semiconductor	Total
per Cell	5.919 kW	5.919 kW
per Converter	266.3 kW	266.3 kW
% Losses	0.59 %	0.59 %

Figure 12 Device Losses & Temperatures

Switching Loss	Single IGBT or Diode Losses during turn on and turn off events (dynamic)
Conduction loss	Single IGBT or Diode Losses during on state (static)
Combined losses	Sum of single IGBT or Diode switching and conduction loss.
Cell losses	Sum of IGBT and Diode losses in a full-bridge cell
Converter losses	Sum of all IGBT and Diode losses
% Losses	Defined as the (%) ratio of calculated combined converter losses with respect to the converter MVA rating i.e., total apparent power flow. Since the converter is meant for a THREE-PHASE application, the kVA rating would correspond to total three-phase AC Power delivered by the converter.
Junction Temperature Avg	Junction temperature average during the simulation period
Junction Temperature Max	Maximum junction temperature during the simulation period

Junction Temperature BLS Junction temperature at a time point just before the last switching, after which the maximum junction temperature is achieved

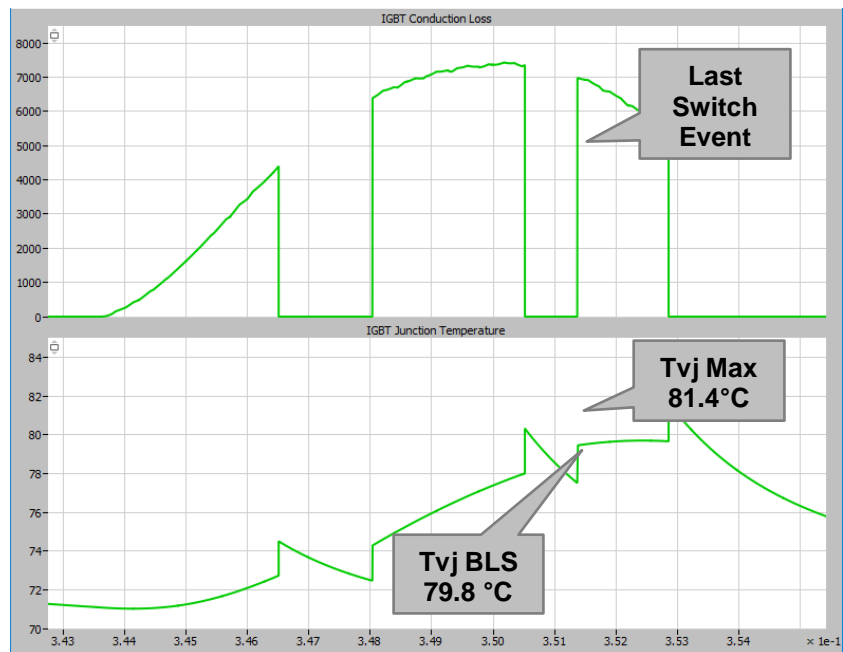


Figure 13 Definition of Tvj before the last switch

Converter AC parameters

Converter AC Parameters					
	Real Power	Reactive Power	Phase Voltage (RMS)	Phase Current (RMS)	Output Frequency
	-0.07 kW	45.05 MVAR	20.81 kV	722 A	50 Hz

Figure 14 Converter AC Parameters

- Real power P Active power / real power output of the converter
- Reactive power Q Q as supplied to load as effective power (reactive) on converter AC side
Calculation see in section 6.4.
- Phase voltage RMS According AC phase value according 1st order harmonics of AC frequency
- Phase current RMS According AC phase value according 1st order harmonics of AC frequency
- Output frequency According to the definition

Control Parameters

Control Parameters				
	Cell Voltage	Switching Frequency	Modulation Index	No. of Cells/phase
	2.433 kV	269 Hz	0.80	15.00

Figure 15 Control Parameters

- Cell Voltage Average capacitor voltage of a cell in a phase leg
- Switching Freq. According to the definition
- Modulation Ind. According to calculations defined in chapter 6.2
- No. of Cells Number of cells per phase leg

5. ALERTS & FEATURES

The system verifies results and generated warning messages in case of limits are violated.

5.1 Junction Temperature

Parameter	Junction temperature
Verification	If the junction temperature BLS of IGBT and/or diode is above its maximum junction temperature limit, the alert message is displayed
Warning message	IGBT temperature out of the safe operating area

5.2 Cell Voltage

Parameter	Cell Voltage
Verification	If the Cell voltage is greater than the safe operating voltage rating of IGBT and/or diode, the alert message is displayed
Warning message	For the voltage rating 1.7kV, Vcellmin = 200V & Vcellmax = 1100V

6. APPLIED CALCULATIONS

6.1 Input Parameter Definitions

V_{cell}	Cell Voltage
n_{cell}	No. of cells in a phase leg
$V_{LL_AC_RMS}$	Line voltage RMS

6.2 Line-Line RMS Voltage of Grid/Load Definition

$$V_{LL_AC_RMS} = \frac{\sqrt{3} * m * n_{cell} * V_{cell}}{\sqrt{2}} \quad \text{for Star connected}$$

$$V_{LL_AC_RMS} = \frac{m * n_{cell} * V_{cell}}{\sqrt{2}} \quad \text{for Delta connected}$$

6.3 Real Power

P_{DC}	DC power / real power absorbed from DC side of VSC calculated according
P_{AC}	real / active power transferred to converter output calculated as:
$V_{TrueRMS}$	True phase voltage RMS AC line to neutral
$I_{TrueRMS}$	True phase current RMS AC
η	Power conversion efficiency

$$V_{trueRMS} = \sqrt{\frac{1}{n} \sum_{v=1}^n \widehat{u}_v^2}$$

It includes all harmonic components NOT ONLY 1st order of output frequency.

$$I_{trueRMS} = \sqrt{\frac{1}{n} \sum_{v=1}^n \widehat{i}_v^2}$$

It includes all harmonic components NOT ONLY 1st order of output frequency.

According to:

$$P_{AC} = \frac{3}{n} \sum_{v=1}^n \widehat{u}_v \cdot \widehat{i}_v \cdot \cos \varphi_v = 3 \cdot V_{trueRMS} \cdot I_{trueRMS} \cdot PF$$

In the FACTS application, real power is equal to zero ideally.

Applied Calculations

6.4 Reactive Power

Q Effective reactive power on the converter AC side [VAr]

$$Q = 3 * V_{Ph_RMS} * I_{Ph_RMS}$$

V_{PH_RMS} Phase voltage (RMS)

I_{PH_RMS} Phase current (RMS)

Defined as the Loss (%) η is the ratio of calculated combined converter losses with respect to the converter input power.

The Q is the main input power definition. Loss (%) η is given by:

$$\eta = \frac{P_{LossConverter}}{Q} * 100\%$$

7. VALIDATION OF SEMIS RESULTS WITH ACTUAL FIELD DATA

To ensure supplied simulation results are reliable, each SEMIS topology is validated with another simulation system or compared to real measurement data.

The total converter semiconductor losses of FACTS Full Bridge MMC with IGBT Star Connected for two cases with different reactive powers are validated with Actual Field Data.

The total converter losses in SEMIS and Actual Field Data are closely matching and the average deviation is well within tolerance (<5%) for both the cases as shown in Figure 16.

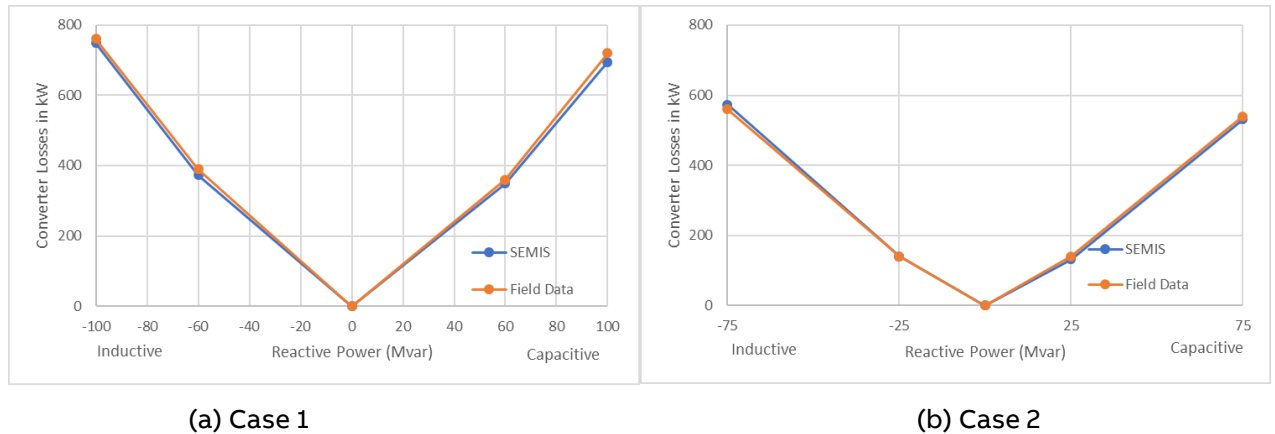


Figure 16 Validation of SEMIS results with Field Data for FACTS Full Bridge MMC with IGBT Star Connected

FACTS Full Bridge MMC with IGBT Delta Connected model is validated with FACTS Full Bridge MMC with IGBT Star Connected model by keeping equal cell voltages and phase currents. Five cases are simulated in both models by varying different parameters like converter line voltage, converter reactive power etc. with the electrical parameters presented in the tables below for comparison.

It was observed that the difference between the electrical parameters is minimal even after the variations in the operating conditions. It was also observed from the total converter losses and the device junction temperatures that the results obtained from both models are very similar and the error percentage is within tolerance (<5%).

Remark:

The deviation in the individual switch loss distribution between FACTS Full Bridge MMC with IGBT Star Connected and Delta Connected is because of phase angle difference between modulating signals in respective models due to topology differences. Also, the phase current contains 3rd harmonic in case of Delta connected STATCOM.

Validation of SEMIS Results with Actual Field Data

Topology		SEMIS 1S FACTS Full Bridge MMC with IGBT Diode														
Y-axis		Phase-to-Phase Measurement														
Date		May 27, 2025														
Device used (unit)		5SYA 2127-2000														
Limit compliance level Green / Yellow / Red		XX / XX / XX														
Installation																
1. Enter all values according to the final results table in the column SEMIS																
2. Enter all values according to the final results from the PSCAD in the column PSCAD																
3. Verify the relative difference; Results must not vary more than 2%																
Description of Settings Set																
Parameter		Sw 1			Sw 2			Sw 3			Sw 4			Sw 5		
Max. Difference [%]		Rolls	Star	Diff%	Rolls	Star	Diff%	Rolls	Star	Diff%	Rolls	Star	Diff%	Rolls	Star	Diff%
				1.5%			1.5%			1.5%			1.5%			1.5%
Device Losses & Temperature																
Switching Losses IGBT 1 [W]																
	356.23	345.32	4.50%	329.84	322.25	1.9%	348.26	308.45	4.95%	448.78	497.76	-1.1%	856.81	799.47	6.93%	
Switching Losses IGBT 2 [W]																
	348.25	325.45	6.85%	333.22	347.45	-4.25%	374.22	389.45	-4.8%	449.47	429.67	4.6%	845.82	798.25	6.2%	
Switching Losses Diode 1 [W]																
	172.28	172.74	-0.27%	187.33	187.58	-0.13%	188.32	183.18	2.75%	27.37	24.84	9.78%	192.73	179.54	7.35%	
Switching Losses Diode 2 [W]																
	184.82	174.24	5.75%	198.37	189.28	4.73%	197.25	182.2	7.67%	26.83	24.32	9.45%	196.88	188.27	4.7%	
Switching Losses IGBT 3 [W]																
	874.74	346.84	1.45%	542.54	587.2	-6.92%	848.54	389.88	1.4%	198.33	198.47	-0.07%	778.33	822.82	-5.4%	
Switching Losses IGBT 4 [W]																
	887.65	896.24	-0.96%	582.83	592.58	-1.66%	852.73	861.22	-0.97%	149.58	142.85	4.67%	785.24	724.81	6.88%	
Switching Losses Diode 3 [W]																
	174.43	174.52	-0.05%	183.45	183.85	-0.22%	164.68	164.67	0.0%	27.44	25.28	7.9%	175.81	174.25	0.9%	
Switching Losses Diode 4 [W]																
	167.1	173.12	-3.55%	186.43	185.45	0.53%	168.48	172.2	-2.2%	23.82	25.25	-5.65%	173.53	184.76	-6.05%	
Conduction Losses IGBT 1 [W]																
	148	148	0.0%	478.82	476.24	0.53%	482	483	-0.21%	232.27	239.37	-2.96%	428.45	424.25	0.99%	
Conduction Losses IGBT 2 [W]																
	148	148	0.0%	478.82	476.24	0.53%	482	483	-0.21%	232.27	239.37	-2.96%	428.45	424.25	0.99%	
Conduction Losses Diode 1 [W]																
	352.27	349.82	0.72%	463.67	468.28	-0.98%	354.86	344.7	2.92%	254.5	256.24	-0.67%	444.68	449.88	-1.35%	
Conduction Losses Diode 2 [W]																
	358.22	349.64	2.53%	459.45	468.4	-1.95%	349.37	342.25	1.75%	252.85	255.25	-0.94%	449.87	449.88	0.0%	
Conduction Losses IGBT 3 [W]																
	148	148	0.0%	477.85	474.45	0.72%	483	483	0.0%	232.83	238.68	-2.45%	424.82	429.38	-1.3%	
Conduction Losses IGBT 4 [W]																
	148	148	0.0%	477.85	474.45	0.72%	483	483	0.0%	232.83	238.68	-2.45%	424.82	429.38	-1.3%	
Conduction Losses Diode 3 [W]																
	348.82	344.7	1.19%	465.28	462.24	0.65%	346.84	339.74	2.07%	234.54	234.32	0.09%	442.45	444.84	-0.54%	
Conduction Losses Diode 4 [W]																
	345.83	339.25	1.95%	464.5	462.88	0.35%	345.83	332.25	3.82%	232.32	234.82	-1.06%	442.24	441.32	0.2%	
Combined Losses IGBT 1 [W]																
	2865	2847	0.63%	1882	198.25	-5.2%	2825	197.8	2.92%	439.25	434.45	1.12%	1288	125.4	4.4%	
Combined Losses IGBT 2 [W]																
	2828	2828	0.0%	1833	193.7	-6.4%	1981	198.8	0.4%	442.3	421.8	4.75%	1273	1228	3.7%	
Combined Losses Diode 1 [W]																
	1428	1428	0.0%	668.8	648.24	3.1%	1428	144.8	-1.4%	388.27	387.85	0.11%	888.45	832.25	6.25%	
Combined Losses Diode 2 [W]																
	1435	1438	-0.21%	654.42	658.46	-0.61%	1427	148.8	-4.6%	389.25	348.88	11.0%	889.32	839.28	6.2%	
Combined Losses IGBT 3 [W]																
	1386	2822	-5.05%	389.85	381.2	2.3%	1349	2812	-5.35%	428.88	437.45	-1.98%	1282	1248	2.73%	
Combined Losses IGBT 4 [W]																
	1334	2818	-5.3%	372.24	387	-4.2%	1327	2812	-5.25%	445.48	444.18	0.29%	1283	125	3.45%	
Combined Losses Diode 3 [W]																
	1418	1418	0.0%	648.82	653.18	-0.66%	1418	148.8	-4.6%	387.38	387.25	0.03%	888.27	836.27	6.2%	
Combined Losses Diode 4 [W]																
	1418	1418	0.0%	651	647.25	0.58%	1418	148.8	-4.6%	387.38	387.25	0.03%	888.27	836.27	6.2%	
Junction Temperature Avg IGBT 1 [C]																
	146.82	144.25	1.73%	73.34	78.8	-6.9%	144.84	149.4	-2.3%	62.43	62.38	0.08%	85.27	83.4	2.15%	
Junction Temperature Avg IGBT 2 [C]																
	144.35	144.85	-0.35%	73.82	78.7	-6.9%	143.73	149.65	-4.9%	62.81	62.82	-0.02%	85.13	83.4	2.0%	
Junction Temperature Avg Diode 1 [C]																
	146.87	145.45	1.0%	88.74	88.26	0.55%	145.64	144.35	0.9%	65.63	65.64	-0.01%	84.65	82.35	2.75%	
Junction Temperature Avg Diode 2 [C]																
	145.35	145.25	0.07%	84.87	88.24	-3.75%	144.32	144.24	0.05%	65.88	65.45	0.64%	84.27	82.35	1.92%	
Junction Temperature Avg IGBT 3 [C]																
	145.75	145.45	0.48%	78.53	78.45	0.1%	142.85	144.22	-1.55%	62.23	62.24	-0.01%	82.87	83.15	-0.32%	
Junction Temperature Avg IGBT 4 [C]																
	145.86	144.42	1.42%	78.31	79.8	-1.89%	142.74	142.81	-0.05%	62.33	62.7	-0.59%	82.31	82.34	-0.03%	
Junction Temperature Avg Diode 3 [C]																
	144.76	145.35	-0.41%	88.32	88.8	-0.55%	143.73	144.8	-1.5%	65.43	65.68	-0.38%	82.48	83.28	-0.96%	
Junction Temperature Avg Diode 4 [C]																
	144.76	145.35	-0.41%	79.35	88.45	-10.1%	143.78	144.85	-1.4%	66.81	65.75	1.67%	82.5	82.65	-0.18%	
Converter Losses [W]																
	678.3	678.8	-0.07%	357.4	355.5	0.53%	549.8	528	4.13%	179.3	177.8	0.84%	444.9	435.2	2.2%	
Losses Efficiency																
	0.98	0.98	0.0%	0.98	0.98	-0.47%	0.93	0.93	0.0%	0.937	0.93	0.84%	0.907	0.88	2.82%	
Control Parameters																
Grid Voltage [V]																
	1828	1888	-3.12%	1945	1946	-0.05%	1888	1812	4.95%	2225	2225	0.0%	3182	3182	0.0%	
Switching Frequency [Hz]																
	263.23	263.23	0.0%	263.23	263.23	0.0%	263.23	263.23	0.0%	153.845	153.845	0.0%	263.23	263.23	0.0%	
Modulation Index																
	0.9	0.9	0.0%	0.9	0.9	0.0%	0.9	0.9	0.0%	0.8	0.8	0.0%	0.8	0.8	0.0%	
Converter MC Parameters																
Duty Cycle Power [MW]																
	39.38	48.4	-19.25%	48.88	48.88	0.0%	74.88	75.88	-1.32%	58.47	58.42	0.08%	88.84	88.4	0.5%	
Phase Voltage RMS [kV]																
	22.82	22.82	0.0%	22.82	22.82	0.0%	16.54	16.52	0.12%	25.82	25.82	0.0%	16.84	16.84	0.0%	
Phase Current RMS [kA]																
	4514	4512	0.04%	383	388	-1.29%	4513	4512	0.02%	668	668	0.0%	833	838	-0.6%	
Output Frequency [Hz]																
	58	58	0.0%	58	58	0.0%	58	58	0.0%	58	58	0.0%	58	58	0.0%	

Figure 17 Validation of FACTS Full Bridge MMC Delta Connected with Star Connected

8. USER MANUAL REVISION HISTORY

Rev.	Page	Change Description	Date / Initial
1.3	all	Included Validation results & Reactive Power Definition	2020-28-04 PGGI/HM
1.2	all	Inclusion of delta connected model	2019-23-12 PGGI/HM
1.1	all	Initial version in new design	2019-23-09 PGGI/HM

9. SIMULATION SOFTWARE RELEASE HISTORY

Rev.	New topic	Fixed defects	Tvj influence	Date
1.1	Reactive Power Definition	-	-	2020-28-04 PGGI/HM
1.0	Initial version	-	-	2019-23-09 PGGI/HM



Contact

**ABB Power Grids Switzerland Ltd.
Semiconductors**

Fabrikstrasse 3
5600 Lenzburg, Switzerland
Phone: +41 58 586 1419
Fax: +41 58 586 1306
E-Mail: abbsem@ch.abb.com

abb.com/semiconductors

Note

We reserve the right to make technical changes or modify the contents of this document without prior notice. With regard to purchase orders, the agreed particulars shall prevail. ABB does not accept any responsibility whatsoever for potential errors or possible lack of information in this document.

We reserve all rights in this document in the subject matter and illustrations contained therein. Any reproduction- in whole or in parts- is forbidden without ABB's prior written consent.